

SPECIFICATION

ANALOG OUTPUTS

Resolution: 12 bits (0 to 4095 decimal).

Channels: Two.

Voltage Output Ranges at 5mA max.

0.0 to 5.0 VDC.

0.0 to 10.0 VDC.

-5.0 to +5.0 VDC.

-10.0 to +10.0 VDC

Current Output Range(with excitation voltage 8-36 VDC).

4 to 20 mA.

Digital-to-Analog Converter:

AD-7548 monolithic chip, double buffered.

Relative Accuracy: +/-1 LSB (includes nonlinearity).

Monotonicity: Guaranteed over operating temperature range.

Settling Time: 4 usec to 0.01% for full-scale step input.

Offset Temperature Drift:

+/-1 ppm/ C. typical.

+/-3 ppm/ C. maximum.

Gain Temperature Drift:

+/-25 ppm/ C. (with reference)

+/-5 ppm/ C. (w/ext. ref)

Reference Voltage Input Range: +/-10V (2 or 4 quadrant)

Reference Input Resistance: 7Kohm min., 11 Kohm typical, 20 Kohm max.

Data Format: Left-justified, two bytes (4 LSB's, then 8 MSB's).

POWER REQUIREMENTS:

+5 VDC at 75 mA typical, 100 mA max.

+12 VDC at 15 mA typical, 25 mA max.

-12 VDC at 25 mA typical, 35 mA max.

ENVIRONMENTAL:

Operating Temperature Range: 0 to +70 C.

Storage Temperature Range: -55 to +125 C.

Humidity: 5% to 95% non-condensing.

Weight: 4 oz.

SIZE: 5.0" long. Can be used in half- or full-size slot.